

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0004] with the following amended paragraph:

[0004] If a gate signal voltage is applied to the TFT-LCD array, the TFT is turned on so that a data voltage having picture image data is applied to a liquid crystal capacitor C_{LC} through the TFT for a turn-on time of the TFT. At this time, current I_{ON} for charging the liquid crystal capacitor is obtained as follows.

$$I_{ON} = \frac{C_{TOT} \cdot V}{\tau_g} \quad \dots\dots\dots(1)$$

Please replace paragraph [0005] with the following amended paragraph:

[0005] In the equation (1), C_{TOT} is a sum of a capacitor by the liquid crystal capacitor C_{LC} and a storage capacitor C_{STO} for maintaining a phase of the liquid crystal until a signal is applied, V is the voltage and τ_g is turn-on time of a gate.

Please replace paragraph [0006] with the following amended paragraph:

[0006] A total amount of charge in the capacitor by the liquid crystal capacitor and the storage capacitor is maintained until a next signal is received after the gate is turned off. Actually, since a leakage current I_{OFF} exists due to resistance R_{OFF} of a channel layer of the TFT, distortion of a liquid crystal applying voltage V_{LC} , i.e., a drop δV_g (a difference between the maximum gate voltage applied when the gate is turned on and the minimum gate voltage applied when the gate is turned off) of the liquid crystal applying voltage

occurs, thereby generating flickering. The leakage current I_{OFF} can be expressed as follows.

$$I_{OFF} = \frac{C_{TOT} \cdot \delta V_g}{\tau_g \cdot N_g} \quad \dots\dots\dots(2)$$